IN THE CLAIMS:

This listing of claims will replace all prior versions and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) ~~An apparatus~~ A personal computer, comprising:

a plurality of inner firewalls comprising [a] at least one hardware component and/or [a] at least one firmware component configured to operate within [a] the personal computer, the personal computer being configured to operate in [a] at least one network of computers,

the personal computer comprising at least two microprocessors and at least two memory components, the at least two microprocessors being located on a single microchip;

at least one of the plurality of inner firewalls being configured to deny access to at least [a] one first microprocessor and at least [a] one first memory component of the personal computer by ~~another~~ at least one other computer through [a] at least one network connection with the personal computer during [a] at least one shared operation, and

~~another~~ at least one of the plurality of inner firewalls being configured to allow access to at least [a] one second microprocessor and at least [a] one second memory component of the personal computer by the at least one other computer through the at least one network connection with the at least one personal computer during the at least one shared operation.

2. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein [a] at least one hardware component and/or software file, and/or firmware file is located within at least one of the plurality of inner firewalls.

3. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the following are grouped together inside at least one of the plurality of inner firewalls:

[a] at least one hardware component and [a] at least one software file; or

[a] at least one hardware component and [a] at least one firmware file; or

[a] at least one software file and [a] at least one firmware file; or

[a] at least one hardware component, [a] at least one software file, and [a] at least one firmware file.

4. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein at least one of the plurality of inner firewalls ~~is substantially a~~ comprises at least one hardware component.

5. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, ~~wherein the personal computer comprises a~~ further comprising at least one dense wave division multiplexing (DWDM) network connection.

6. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, ~~wherein the personal computer comprises a~~ further comprising at least one device enabling at least one wireless network connection.

7. (Currently Amended) The ~~apparatus~~ personal computer of claim 6, ~~wherein the personal computer comprises a~~ further comprising at least one hardware encryption component.

8. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, ~~wherein the personal computer includes an~~ further comprising at least one operating system comprising more than one independent component, ~~one or more of the components~~ at least one of the more than one independent component having its own firewall.

9. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, ~~wherein the personal computer includes an~~ further comprising at least one operating system comprising more than one independent component, ~~one or more of the components~~ each independent component having its own firewall.

10. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, ~~wherein the personal computer includes an~~ further comprising at least one application program comprising more than one independent component, ~~one or more of the components~~ at least one of the more than one independent component having its own firewall.

11. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, ~~wherein the personal computer includes an~~ further comprising at least one application program comprising more than one independent ~~components~~ component, ~~one or more of the components~~ each independent component having its own firewall.

12. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein all files of [a] at least one network-accessible portion of volatile memory of the personal computer are erased when control of the at least one network-accessible portion is transferred between the at least one network of computers and [a] at least one user of the personal computer, the at least one network-accessible portion being located outside at least one of the plurality of inner firewalls.

13. (Currently Amended) The ~~apparatus~~ personal computer of claim 12, wherein the ~~file erasure is accomplished by one of~~ volatile memory of the personal computer is erased by power interruption and/or overwriting.

14. (Currently Amended) The ~~apparatus~~ personal computer of claim of 1, wherein all files in [a] at least one network-accessible portion of [a] at least one non-volatile memory of the personal computer are erased when control of the at least one network-accessible portion is transferred between the at least one network and [a] at least one user of the personal computer, the at least one network-accessible portion being located outside at least one of the plurality of inner firewalls.

15. (Currently Amended) The ~~apparatus~~ personal computer of claim 14, wherein the at least one of the plurality of inner ~~firewall~~ firewalls is located on the single microchip.

16. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one first memory component comprises [a] at least one system BIOS.

17. (Currently Amended) The ~~apparatus~~ personal computer of claim 18, wherein the single microchip comprises ~~an~~ at least one encryption component.

18. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the single microchip has at least four or 8 or 16 or 32 or 64 or 128 or 256 or 512 or 1024 microprocessors.

19. (Currently Amended) The ~~apparatus~~ personal computer of claim 18, wherein the single microchip comprises [a] at least one field-programmable gate array (FPGA) and/or active configuration of an integrated circuit.

20. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one network of computers comprises [a] the World Wide Web and/or [an] the Internet.

21. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one network connection comprises [an] at least one optical fiber connection ~~substantially~~ directly to the personal computer.

22. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one first memory component is [a] at least one flash memory device.

23. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is [a] at least one flash memory device.

24. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is [a] at least one random access memory (RAM) device.

25. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is [a] at least one hard drive device.

26. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is [a] at least one read-only compact disk drive (CD-ROM) device.

27. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is [a] at least one read-only digital video disk drive (DVD) device.

28. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is volatile memory.

29. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is non-volatile memory.

30. (Currently Amended) The ~~apparatus~~ personal computer of claim 29, wherein the non-volatile memory comprises [a] at least one magnetic random access memory (MRAM) and/or ovonic memory.

31. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one first memory component is non-volatile memory.

32. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component duplicates [a] the at least one first memory component.

33. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one first memory component is read and write memory.

34. (Currently Amended) The ~~apparatus~~ personal computer of claim 1, wherein the at least one second memory component is read-only memory.

35. (Currently Amended) An ~~apparatus~~ personal computer, comprising:

a plurality of inner firewalls configured to operate within [a] the personal computer,

the personal computer being configured to operate in [a] at least one network of computers,

the personal computer comprising at least two microprocessors,

at least one of the plurality of inner firewalls being configured to deny access to at least [a] one first microprocessor of the personal computer by ~~another~~ at least one other computer

through [a] at least one network connection with the personal computer during [a] at least one

shared operation, and

another at least one of the plurality of inner firewalls being configured to allow access to

at least [a] one second microprocessor of the personal computer by the at least one other

computer through the at least one network connection with the personal computer during the at

least one shared operation.

36. (Currently Amended) An ~~apparatus~~ personal computer, comprising:

at least one microchip comprising a plurality of inner firewalls configured to operate

within [a] the personal computer, the personal computer being configured to operate in [a] at

least one network of computers,

~~the personal computer~~ the at least one microchip comprising at least two microprocessors

and at least two memory components,

at least one of the plurality of inner firewalls being configured to deny access to at least

[a] one first microprocessor and at least [a] one first memory component of the personal

computer by ~~another~~ at least one other computer through [a] at least one network connection

with the personal computer during [a] at least one shared operation, and

another at least one of the plurality of inner firewalls being configured to allow access to

at least [a] one second microprocessor and at least [a] one second memory component of the

personal computer by the at least one other computer through the at least one network connection

with the personal computer during the at least one shared operation.

37. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 35, wherein at least one of the <u>plurality of</u> inner firewalls comprises [a] <u>at least one</u> hardware component and/or [a] <u>at least one</u> firmware component.

38. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 36, wherein at least one of the <u>plurality of</u> inner firewalls comprises [a] <u>at least one</u> hardware component and/or [a] <u>at least one</u> firmware component.

39. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 37, wherein at least two or four or 8 or 16 or 32 or 64 or 128 or 256 or 512 or 1024 microprocessors are located on a single microchip.

40. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 38, wherein at least two or four or 8 or 16 or 32 or 64 or 128 or 256 or 512 or 1024 microprocessors are located on a single microchip.

41. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 39, wherein the <u>single</u> microchip comprises [an] <u>at least one</u> encryption component.

42. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 40, wherein the <u>single</u> microchip comprises [an] <u>at least one</u> encryption component.

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43. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 39, wherein the <u>single</u> microchip comprises [a] <u>at least one</u> field-programmable gate array (FPGA) and/or active configuration of [an] <u>at least one</u> integrated circuit.

44. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 40, wherein the <u>single</u> microchip comprises [a] <u>at least one</u> field-programmable gate array (FPGA) and/or active configuration of [an] <u>at least one</u> integrated circuit.

45. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 39, wherein the <u>single</u> microchip comprises [a] <u>at least one</u> master control and/or <u>at least one</u> processing microprocessor.

46. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 40, wherein the <u>single</u> microchip comprises [a] <u>at least one</u> master control and/or <u>at least one</u> processing microprocessor.

47. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 18, wherein the <u>single</u> microchip comprises [a] <u>at least one</u> master control and/or <u>at least one</u> processing microprocessor.

48. (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 39, wherein the <u>single</u> personal computer comprises [a] <u>at least one device enabling at least one</u> wireless network connection.

49.   (Currently Amended) The ~~apparatus~~ <u>personal computer</u> of claim 40, wherein the <u>single</u> personal computer comprises [a] <u>at least one device enabling at least one</u> wireless network connection.

50.   (New) The personal computer of claim 15, wherein at least one first memory component is located on the single microchip.

51.   (New) The personal computer of claim 15, wherein at least one second memory component is located on the single microchip.

52.   (New) The personal computer of claim 35, wherein at least two of the plurality of inner firewalls and the at least two microprocessors are located on the single microchip.